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01/09/2002

JS
866

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPLICATION		FILING DATE		CLASS	SUBCLASS	GAU	EXAMINER
APPL NUM	10040378	FILING DATE	01/09/2002	CLASS	246	GAU	1746 Vinh
**APPLICANTS: Komada Daisuke;							
**CONTINUING DATA VERIFIED:							
** FOREIGN APPLICATIONS VERIFIED: verified LV JAPAN 2001-300562 09/28/2001							
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		107317-00040			
Verified and Acknowledged Examiners's initials							
TITLE : Manufacture method for semiconductor device having silicon-containing insulating film							

U.S. DEPT. OF COMM. PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	Print Claim for O.G.
Amount Due	Date Paid	DRAWING	
		Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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